

FIG.1

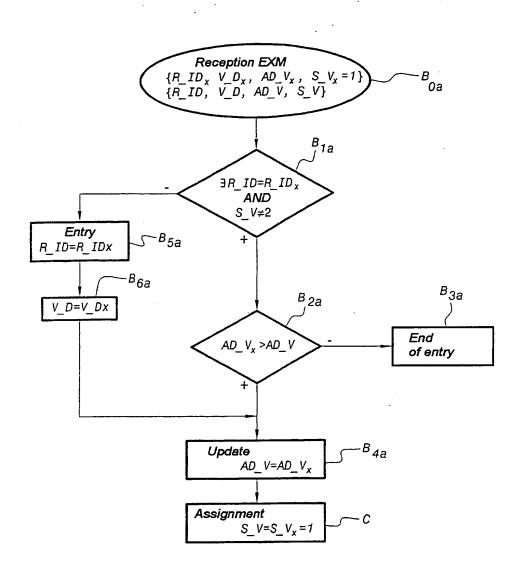


FIG.2a
Entry of an enabled right

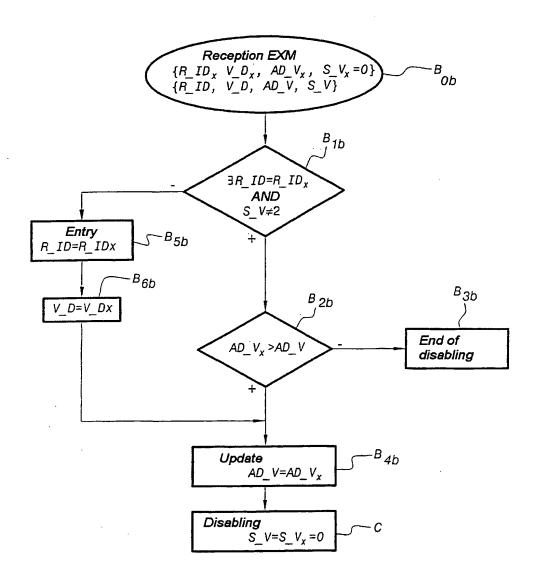


FIG.2b
Disabling of a right

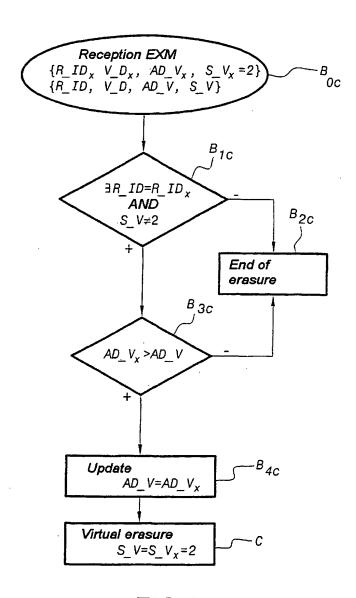
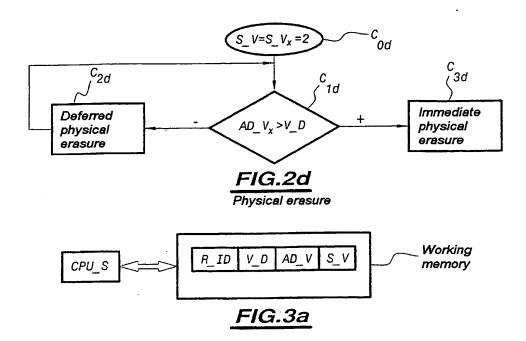


FIG.2c
Virtual erasure of a right



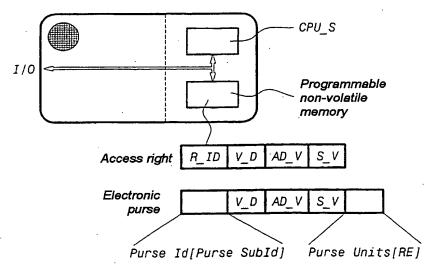


FIG.3b